

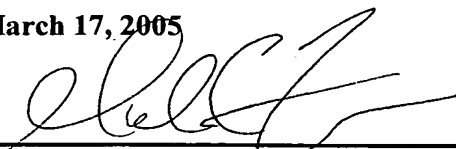


IPW

PATENT  
Docket No. INTEL/17852

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicants: **ZIMMER, et al.**  
U.S. Serial No.: 10/719,428  
For: "Methods and Apparatus to  
Provide Protection for Firmware  
Resources"  
Filed: 11/21/2003  
Assignee: Intel Corporation  
Group Art Unit: 2131  
Examiner: Unknown

) I hereby certify that this paper and the  
) documents referred to as enclosed  
) therewith are being deposited with the  
) United States Postal Service as first  
) class mail, postage prepaid, in an  
) envelope addressed to Commissioner  
) for Patents, P.O. Box 1450,  
) Alexandria, Virginia 22313-1450 on  
) this date:  
)  
) **March 17, 2005**  
)  
)   
)  
) **Mark C. Zimmerman**  
) **Attorney for Applicants**  
) **Registration No. 44,006**  
)

**STATUS LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

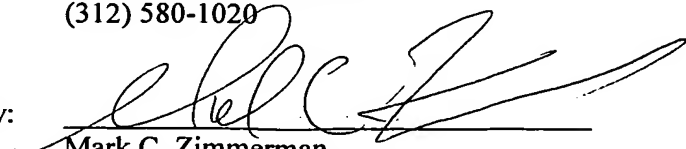
Kindly advise when an Office action can be expected in the above-referenced  
matter. Please note that it has been more than three months since the last communication.

Respectfully submitted,

Hanley, Flight & Zimmerman, LLC  
(at customer number 34431)  
20 North Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

March 17, 2005

By:

  
**Mark C. Zimmerman**  
**Registration No.: 44,006**

Attorneys for Intel Corporation